

Fig.1

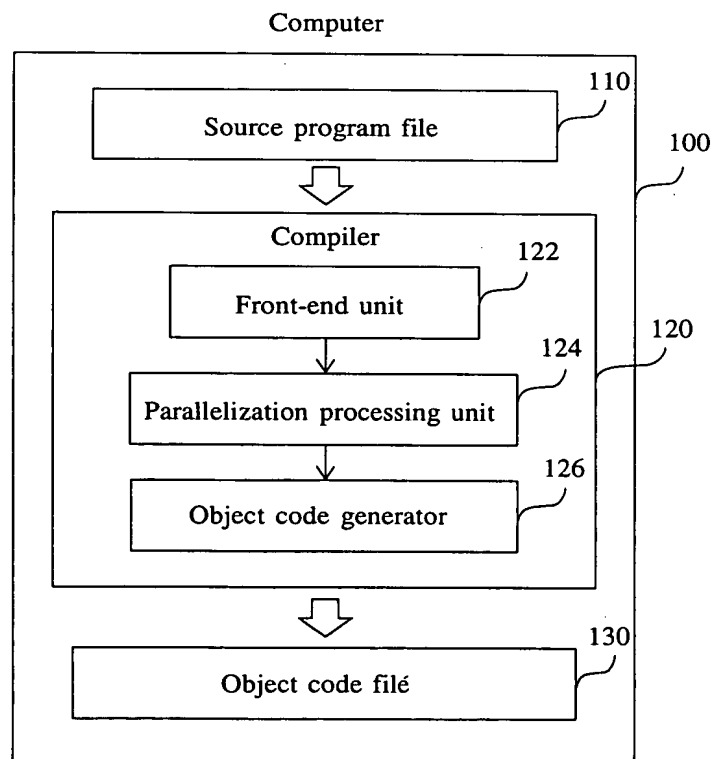


Fig.2

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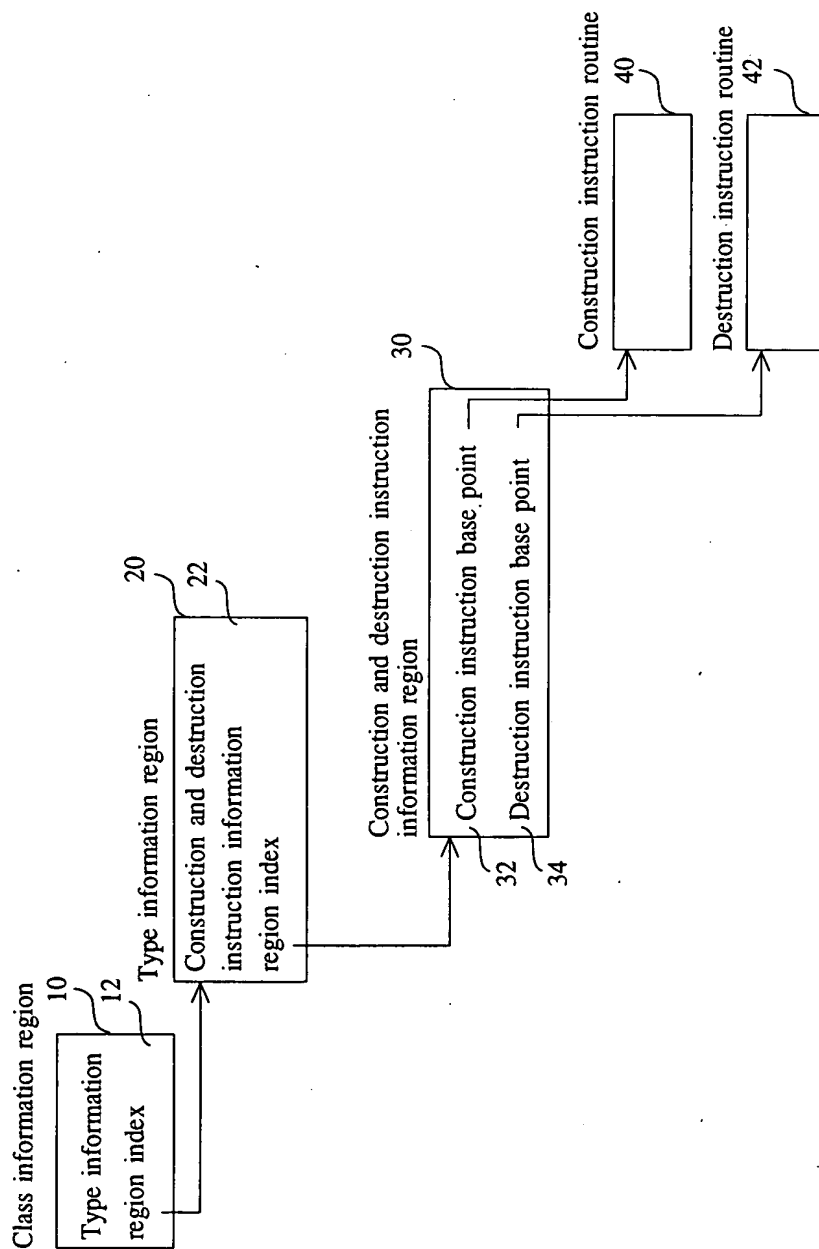


Fig.3

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graph TD
    Start([Start]) --> S1{Automatic  
parallelization processing  
is performed ?}
    S1 -- Yes --> S13{Are there  
class-type  
variables ?}
    S1 -- No --> S3{Is a parallelization  
directive allowed ?}
    S13 -- No --> S3
    S13 -- Yes --> S7[Allocate construction and destruction instruction information region,  
and set construction and destruction instruction information region  
index in type information region]
    S3 -- No --> S3
    S3 -- Yes --> S5{Is the variable specified in  
the clause of the parallelization  
directive a class-type variable ?}
    S5 -- No --> S3
    S5 -- Yes --> S7
    S7 --> S9[Read out construction instruction routine and  
destruction instruction routine from type information region]
    S9 --> S11[Set construction instruction base point for construction  
instruction routine and destruction instruction base point  
for destruction instruction routine in construction and  
destruction instruction information region]
    S11 --> Return([Return])

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Fig.4

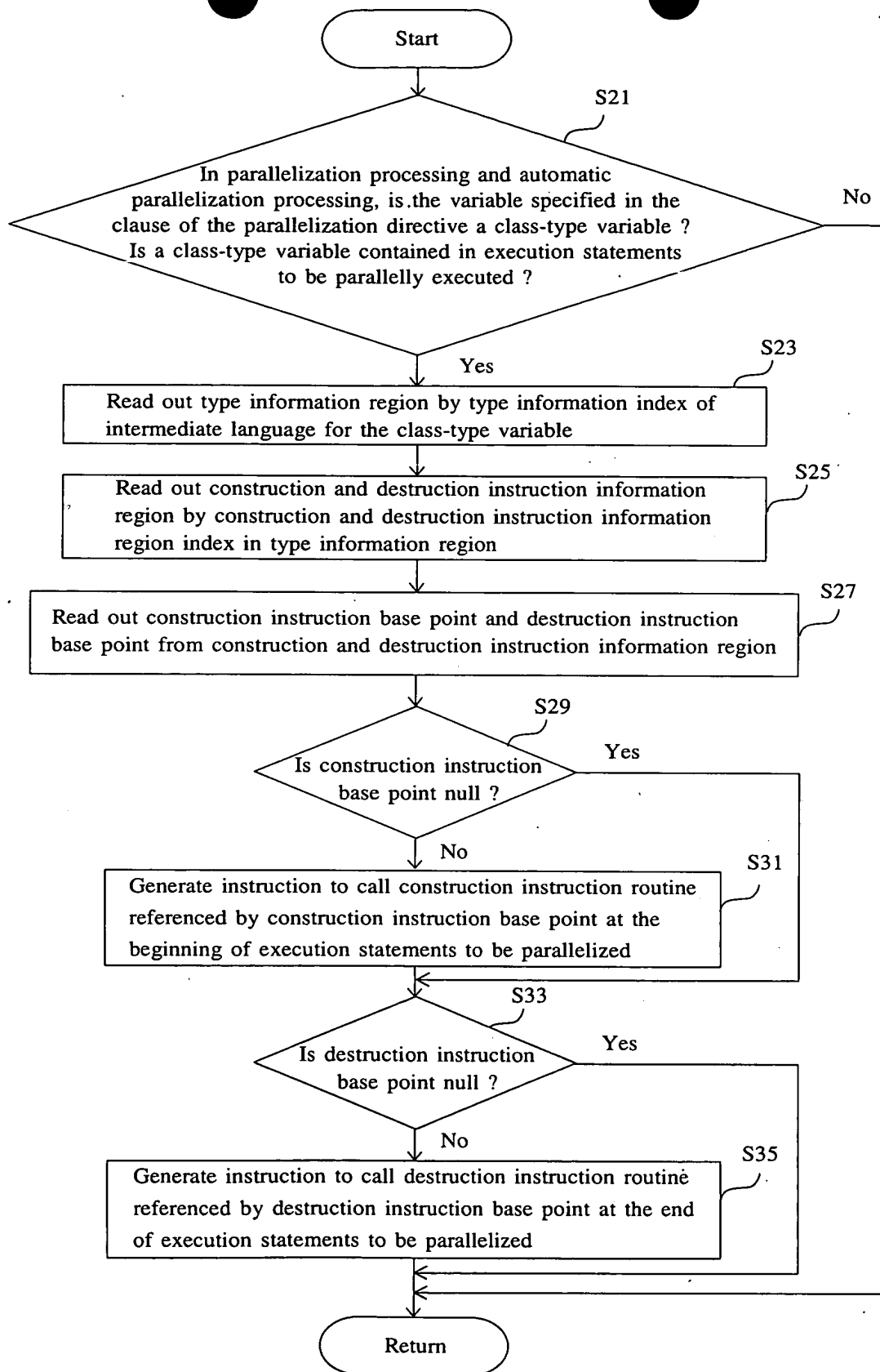


Fig.5